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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: T. NOTO et al
Application No.: 10/542,727
Filed: July 20, 2005
For: SEMICONDUCTOR INTEGRATED CIRCUIT
Art Unit: 2819
Exr.: Exr. V. TAN

AMENDMENT

Commissioner for Patent
P.O. Box 1450
Alexandria, VA 22313-1450

April 27, 2007

Sir:

In response to the Office Action dated October 27, 2006, the period of response for which extension is requested by the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Claims; and

Remarks are Included following the amendment.